

Publications: RK Shyamasundar (Rudrapatna Kallikote Shyamasundar)

A. Patents:

1. METHOD FOR PROTECTING USER MANAGED MEMORY USING EXCEPTION MODEL, **US patent applied, 2008 (Invention Achievement Award)**
2. COMPILE-TIME AFFINITY COMPUTATION FRAMEWORK DEVICES AND METHODS , **US Patent applied, 2008**
3. A tool-transparent, feedback-driven, adaptive method and apparatus for providing Static-Code-Analysis as a Service, **Filed for US patent, 2008**
4. Concurrent Static Single Assignment for General Barrier Synchronization Programming Languages, US Patent filed, 2009
5. Automatic Checkpointing and Role Based Realization of Software Transactional Memory, US patent filed, 2009
6. Federated Two-Factor Identity & Risk Management Ecosystem, 3 Indian patents filed, 2007
7. Unified Content and Platform Delivery, Filed Dec 2009

B. Monographs

8. RK Shyamasundar and S Ramesh, *RealTime Programming Languages: Specification and Verification*, Manuscript 2008, for World Scientific Publishers, Singapore, Nov 2009

C. Journals

9. Anup Kumar Bhattacharjee and R.K. Shyamasundar: "Activity Diagrams: A Formal Framework to Model Business Processes and Code Generation", in *Journal of Object Technology*, vol. 8, no. 1, Januar-Februar 2009, pp. 189-220
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