

## Computational Complexity - Lecture 12.

Recap: - Polynomial hierarchy

$$\text{Thm: } \Sigma_i = \Pi_i \Rightarrow \text{PH} = \Sigma_i.$$

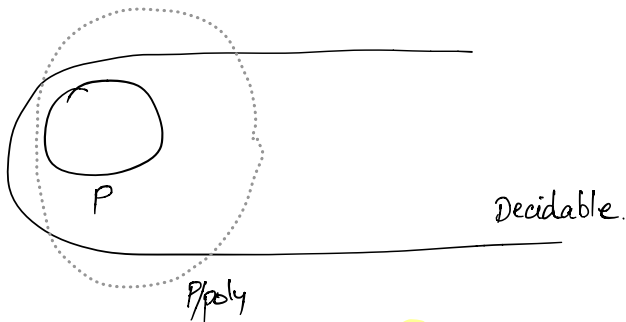
- Circuit families:  $C = \{C_i\}_{i=1, \dots}$  with  $C_i$  "handling" length  $i$  strings

-  $\text{SIZE}(f(n)) = \{L : L \text{ is decided by a circuit family of size } S(n)\}$

$$\text{P/poly} = \bigcup_{c \geq 0} \text{SIZE}(n^c)$$

Agenda: - Karp-Lipton (Sipser) Theorem

- More on circuits & advice.



Where is NP here?

Thm: [Karp-Lipton] If  $\text{NP} \subseteq \text{P/poly}$ , then  $\text{PH} = \Sigma_2$ .

Pf: We'll show that  $\text{NP} \subseteq \text{P/poly}$  then  $\Sigma_2 = \Pi_2$ .

$$L \in \Pi_2 \quad x \in L \Leftrightarrow \forall y \exists z. M(x, y, z) = 1 \quad \varphi'$$

$$L' = \{(x, y) : \exists z. M(x, y, z) = 1\} \in \text{NP}$$

$\Rightarrow$  By the hyp. there is a circuit family  $\{C_i\}$  deciding  $L'$ .

No clue of what  $\{C_i\}$  is ... but we can guess!

We can use this  $\{c_i\}$  to find a  $z$ . if one exists.

$$\varphi \circ \exists c_1, \dots, c_m \quad \forall y \quad M(x, y, \text{GetWitness}(c_1, \dots, c_m, x, y)) = 1$$

Claim:  $\varphi$  is true iff  $\varphi'$  is true

Pf:  $\varphi' = \text{true} \Rightarrow \varphi$  is true.

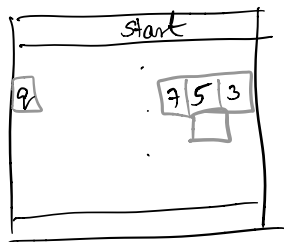
$\varphi = \text{true} \Rightarrow \varphi'$  is true

$$\therefore \Pi_2 \subseteq \Sigma_2 \Rightarrow PH = \Sigma_2 \cap \Pi_2. \quad \square.$$

An extension of this theorem

Thm [Meyer] If  $EXP \subseteq P/poly$  then  $EXP = \Sigma_2$

Pf:  $M$ -TM that runs in exp. time. ( $2^{n^c}$ )



$$L_M = \left\{ (x, t, s) : \begin{array}{l} s^{\text{th}} \text{ symbol in the } t^{\text{th}} \\ \text{row, when } M \text{ starts on } \\ x \text{ is } 1. \end{array} \right\}$$

$$L_M \in EXP \subseteq P/poly$$

$\Rightarrow$  There is some circuit family.

$$|x| = n$$

$$\exists C \quad \forall t, s. \text{ Local Check}(C, x, t, s)$$

$\wedge$  Start state ( $\perp$ )

$\wedge$  Final state (accept)

$$\Rightarrow EXP = \Sigma_2. \quad \square.$$

Hierarchy theorems?

Is  $\text{SIZE}(f(n)) \subsetneq \text{SIZE}(g(n))$  if  $f(n) \ll g(n)$ ?

Usual diagonalization doesn't quite work. (why?).

Thm: For any  $f, g$  with  $n < 10f(n) < g(n) < 2^n/n$ ,  
we have  $\text{SIZE}(f(n)) \subsetneq \text{SIZE}(g(n))$ .

Revisiting the quiz:

▷ How many fns  $F: \{0,1\}^l \rightarrow \{0,1\}$  are there?  
 $2^l$

▷ How many circuits are there of size  $s$ ?  
gates  $1, 2, \dots, s$

For each gate:

▷ type  $\wedge, \vee, \neg$   $O(1)$

▷ left child  $\log s$   
right child  $\log s$

Description:  $\leq 3s \log s \Rightarrow \# \text{circuits} = 2^{3s \log s}$

Corollary: There are fns  $F: \{0,1\}^l \rightarrow \{0,1\}$  that  
cannot be computed by  $S = 2^{l/10}$  size.

P.f.:  $3s \log s \leq 3 \cdot 2^{l/10} \cdot l \leq 2^{l/3} \Rightarrow \# \text{circuits} \leq 2^{2^{l/3}}$

But there are  $2^{2^l}$  functions! HPP  $\Rightarrow$  Done!  $\square$

Note: A "random" function is probably very hard.  
"Pseudorandom property"

Thm: For any  $f, g$  with  $n < 10f(n) < g(n) < 2^n/n$ ,  
we have  $\text{SIZE}(f(n)) \not\subseteq \text{SIZE}(g(n))$ .

Pf: Choose an  $l$  carefully

Claim 1: There are functions  $F: \{0,1\}^l \rightarrow \{0,1\}$   
that cannot be comp by size  $2^l/10l$ .

Claim 2: Every function  $F: \{0,1\}^l \rightarrow \{0,1\}$  can  
be comp by circuits of size  $2^l/l$ .

Set  $l$ :  $f(n) \lesssim 2^l/10l$

$F': \{0,1\}^n \rightarrow \{0,1\}$  defined by  $F'(x) = F(\text{first } l \text{ bits of } x)$

$\square$

About the power of P/poly.

$L \in \text{P/poly}$  if there exists  $\{C_i\}_{i=1, \dots}$

that accepts  $L$ .

Varian: But we aren't saying anything about how hard it is to construct  $C_n$  given  $n$ .

↳ Non-uniform model  
Crypto!

Uniform Circuit families:  $\{C_i\}_{i=1, \dots}$  is a  $*$ -uniform circuit family if the function  $i \mapsto C_i$  can be computed in  $*$ .

Replace  $*$  by your favourite class.

Go back and check:  $P \subseteq \underline{=} L\text{-uniform } P/\text{poly}$ .

Why  $P/\text{poly}$ ? Is there a  $P/\log n$ ?

TMs with advice.

$\text{DTIME}(t(n))/a(n)$

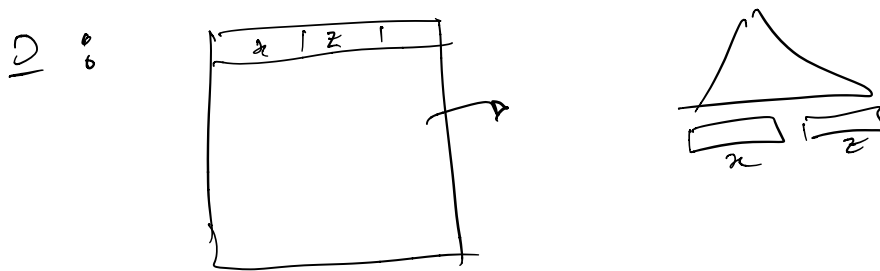
Languages accepted by a  $\text{DTIME}(t(n))$  machine when given an advice.

$\left\{ L : \exists M \in \text{DTIME}(t(n)), \exists \{z_i\}_{i=1, \dots} \text{ such that } \right.$   
 $\left. |z_i| \leq a(i) \text{ and } x \in L \text{ iff } M(x, z_{|x|}) = 1. \right\}$

Eg:  $\text{UHALT} = \{ 1^n : \text{The } n^{\text{th}} \text{ machine halts on a blank tape} \}$   
 $\in P/\perp$ .

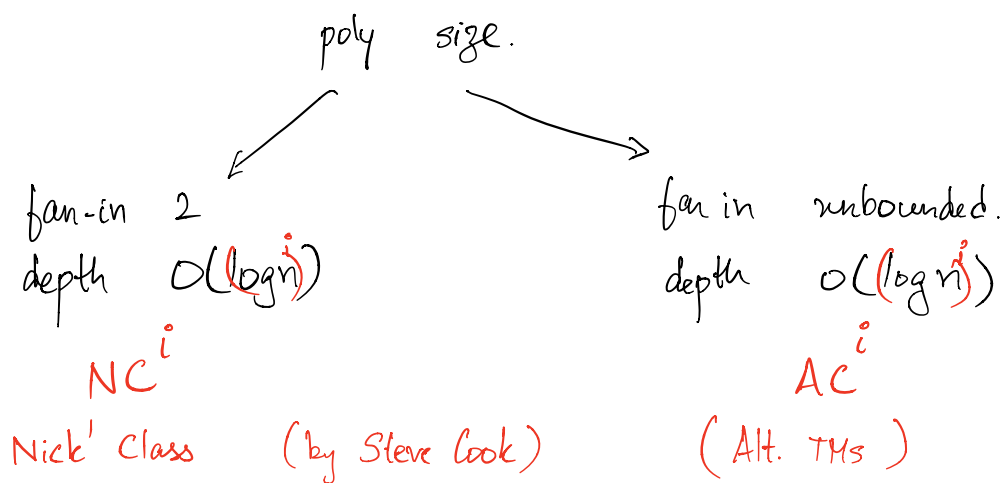
Thm:  $P/\text{poly} = \bigcup_{c,d} \text{DTIME}(n^c)/n^d$

Pf:  $\subseteq$ : The advice strings  $\{z_i\}$  is just the desc. of the circuit family.  
 &  $M$  is Ckt Eval



□

Some important circuit classes:



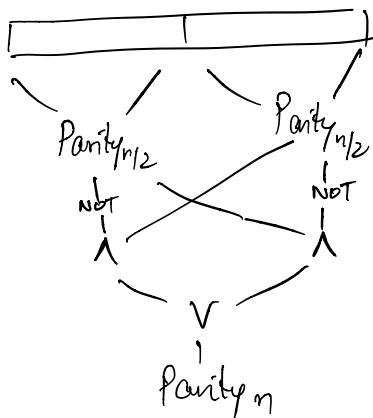
$AC^0$ : constant depth circuits, unbounded fan in, poly size.

$NC^0$ : constant depth circuits, bounded fan-in depends only on  $O(1)$  bits! Boring.

$NC^1$ : bounded fan-in log-depth, poly size.

Obs:  $PARITY \in NC^1$ .

Pf:



Size =  $\text{poly}(n)$

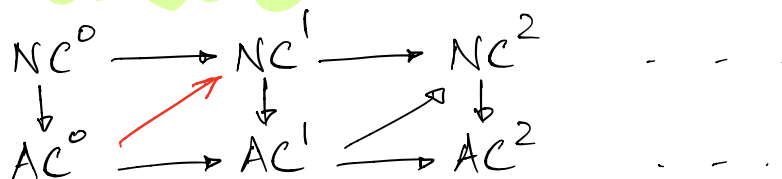
Depth =  $O(\log n)$

fan-in = 2.

□.

□.

Thm: [Furst-Saxe-Sipser, Hastad, Razborov, Smolensky]  
 $PARITY \notin AC^0$ .



$ACC$ :  $AC$  class with  $\wedge, \vee, \neg, \text{Mod}_b$  gates

Does it make sense to study large circuits?  
say exponential size?  
 $S = 2^{n^c}$     $i = 2^{n^c}$

Defn: (Direct connection uniformity).

$\{C_n\}$  is a DC-uniform family if there is a poly-time algorithm to do the following tasks:

Type ( $i$ , "g") : What is the type of gate 'g'.

Child ( $i$ , "g<sub>1</sub>", "g<sub>2</sub>") : Is g<sub>1</sub> a child of g<sub>2</sub>?

Size ( $i$ ) : What is the size of  $C_i$ ?

Makes sense even for exponential size circuits.

Thm:  $L \in PH$  iff  $L$  is computed by a DC-uniform circuit family  $\{C_i\}$  with

▷  $\text{size}(C) \leq 2^{\text{poly}(n)}$

▷  $\{C_i\}$  is a constant depth family with unbounded fan-in.

▷ All NOT gates pushed to the inputs.

(Hence the name AC).

Also, if we remove the const. depth requirement, we get EXP.

Next class: Randomised computation